

Notice of References Cited	Application/Control No. 09/703,430		Applicant(s)/Patent Under Reexamination CHO ET AL.	
	Examiner Frank Duong		Art Unit 2666	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,081,654	01-1992	Stephenson et al.	375/368
	B	US-6,466,886	10-2002	Marmur, Oren	702/126
	C	US-6,631,144	10-2003	Johansen, Henrik Ingvar	370/516
	D	US-6,526,109	02-2003	Chang et al.	375/371
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Belot et al, A 3.3-V Power Adaptiver 1244/622/155 Mbit/s Transceiver for ATM, SONET/SDH, IEEE pages 1047-1058, 1998
	V	Seo et al, A 3V Low Power 156/622/1244 Mbps CMOS Parallel Clock and Data Recovery Circuit for Optical Communications, IEICE, pages 1720-1727, 2000.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.